

# Interference Search

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L22	187	fin adj (fet\$1 (field adj effect adj transistor\$1)) dual adj gate\$1 adj transistor\$1	US-PGPUB	OR	OFF	2005/09/29 13:01
L23	27479	(hole\$1 via\$1 opening\$1 cavit\$3 recess\$2) with gate\$1	US-PGPUB	OR	OFF	2005/09/29 13:02
L24	99978	(hole\$1 via\$1 opening\$1 cavit\$3 recess\$2) with (remov\$3 etch\$3)	US-PGPUB	OR	OFF	2005/09/29 13:03
L25	4	(22 same 23 same 24).clm.	US-PGPUB	OR	OFF	2005/09/29 13:03

# General Search

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	612	fin adj (fet\$1 (field adj effect adj transistor\$1)) dual adj gate\$1 adj transistor\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/29 10:14
L2	233252	(hole\$1 via\$1 opening\$1 cavit\$3 recess\$2) with gate\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/29 10:18
L3	225	1 and 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/29 10:17
L4	106	3 and (hole\$1 via\$1 opening\$1 cavit\$3 recess\$2) with (remov\$3 etch\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/29 10:19

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	273	(438/157).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/09/29 14:34
L2	57	(438/176).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/09/29 14:34
L3	152	(438/183).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/09/29 14:34
L5	282	(438/283).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/09/29 14:42
L6	436	(438/587).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/09/29 14:35

Day : Thursday  
Date: 9/29/2005


**PALM INTRANET**

Time: 13:05:54

**Inventor Name Search Result**

Your Search was:

Last Name = FURUKAWA

First Name = TOSHIHARU

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>06900894</u>	<u>4697532</u>	150	08/27/1986	OPERATING METHOD FOR A REFUSE PROCESSING FURNACE	FURUKAWA, TOSHIHARU
<u>08011496</u>	<u>5409434</u>	150	01/29/1993	CONTROL SYSTEM FOR SHIFT-BY-WIRE AUTOMATIC TRANSMISSION	FURUKAWA, TOSHIHARU
<u>08357276</u>	<u>5462830</u>	150	12/13/1994	RESIN COMPOSITION FOR A TONER FOR AN ELECTROPHOTOGRAPHY AND THE TONER COMPRISED THE SAME	FURUKAWA, TOSHIHARU
<u>08370703</u>	<u>5643822</u>	250	01/10/1995	METHOD FOR FORMING TRENCH-ISOLATED FET DEVICES	FURUKAWA, TOSHIHARU
<u>08380407</u>	<u>5505674</u>	150	01/30/1995	CONTROL SYSTEM WITH FAILSAFE RANGE PASSAGES IN A CHANGEOVER VALVE FOR SHIFT-BY-WIRE AUTOMATIC TRANSMISSION	FURUKAWA, TOSHIHARU
<u>08556194</u>	<u>5622080</u>	150	11/09/1995	SAFETY INTERLOCK FOR PREVENTING DOUBLE ENGAGEMENT OF FORWARD AND REVERSE DOG CLUTCHES AND SHAFT CLUTCH	FURUKAWA, TOSHIHARU
<u>08667210</u>	<u>5729052</u>	150	06/20/1996	INTEGRATED ULSI HEATSINK	FURUKAWA, TOSHIHARU
<u>08681104</u>	Not Issued	166	07/22/1996	TRENCH-ISOLATED FET DEVICES, AND METHOD FOR THEIR MANUFACTURE	FURUKAWA, TOSHIHARU
<u>08838539</u>	<u>5773362</u>	250	04/09/1997	METHOD OF MANUFACTURING AN INTEGRATED ULSI HEATSINK	FURUKAWA, TOSHIHARU

<u>08929730</u>	<u>5956597</u>	150	09/15/1997	METHOD FOR PRODUCING SOI & NON-SOI CIRCUITS ON A SINGLE WAFER	FURUKAWA, TOSHIHARU
<u>08931222</u>	<u>5798553</u>	150	09/16/1997	TRENCH-ISOLATED FET DEVICES, AND METHOD FOR THEIR MANUFACTURE	FURUKAWA, TOSHIHARU
<u>08959779</u>	<u>6007968</u>	150	10/29/1997	METHOD FOR FORMING FEATURES USING FREQUENCY DOUBLING HYBRID RESIST AND DEVICE FORMED THEREBY	FURUKAWA, TOSHIHARU
<u>09014523</u>	<u>5831301</u>	150	01/28/1998	TRENCH STORAGE DRAM CELL INCLUDING A STEP TRANSFER DEVICE	FURUKAWA, TOSHIHARU
<u>09024840</u>	<u>5998835</u>	150	02/17/1998	HIGH PERFORMANCE MOSFET DEVICE WITH RAISED SOURCE AND DRAIN	FURUKAWA, TOSHIHARU
<u>09056903</u>	<u>5945707</u>	150	04/07/1998	DRAM CELL WITH GROOVED TRANSFER DEVICE	FURUKAWA, TOSHIHARU
<u>09070633</u>	<u>6017810</u>	150	04/30/1998	PROCESS FOR FABRICATING FIELD EFFECT TRANSISTOR WITH A SELF-ALIGNED GATE TO DEVICE ISOLATION	FURUKAWA, TOSHIHARU
<u>09071704</u>	Not Issued	161	05/01/1998	METHOD OF DOPING A GATE AND CREATING A VERY SHALLOW SOURCE/DRAIN EXTENSION	FURUKAWA, TOSHIHARU
<u>09072031</u>	<u>6210866</u>	150	05/04/1998	METHOD FOR FORMING FEATURES USING SELF-TRIMMING BY SELECTIVE ETCH AND DEVICE FORMED THEREBY	FURUKAWA, TOSHIHARU
<u>09078118</u>	<u>6184041</u>	150	05/13/1998	FUSED HYBRID RESIST SHAPES AS A MEANS OF MODULATING HYBRID RESIST SPACE WIDTH	FURUKAWA, TOSHIHARU
<u>09082886</u>	<u>6014422</u>	250	05/21/1998	NOVEL METHOD FOR VARYING X-RAY HYBRID RESIST SPACE DIMENSIONS	FURUKAWA, TOSHIHARU
<u>09085965</u>	<u>6107133</u>	150	05/28/1998	METHOD FOR MAKING A FIVE SQUARE VERTICAL DRAM CELL	FURUKAWA, TOSHIHARU
<u>09086057</u>	<u>6225158</u>	150	05/28/1998	TRENCH STORAGE DYNAMIC RANDOM ACCESS MEMORY CELL WITH VERTICAL TRANSFER DEVICE	FURUKAWA, TOSHIHARU

<u>09086174</u>	<u>6190988</u>	150	05/28/1998	METHOD FOR A CONTROLLED BOTTLE TRENCH FOR A DRAM STORAGE NODE	FURUKAWA, TOSHIHARU
<u>09089650</u>	<u>6221704</u>	150	06/03/1998	PROCESS FOR FABRICATING SHORT CHANNEL FIELD EFFECT TRANSISTOR WITH A HIGHLY CONDUCTIVE GATE	FURUKAWA, TOSHIHARU
<u>09092749</u>	<u>6614074</u>	150	06/05/1998	GROOVED PLANAR DRAM TRANSFER DEVICE USING BURIED POCKET	FURUKAWA, TOSHIHARU
<u>09105739</u>	<u>6767789</u>	150	06/26/1998	METHOD FOR INTERCONNECTION BETWEEN TRANSFER DEVICES AND STORAGE CAPACITORS IN MEMORY CELLS AND DEVICE FORMED THEREBY	FURUKAWA, TOSHIHARU
<u>09126412</u>	<u>6121651</u>	150	07/30/1998	DRAM CELL WITH THREE-SIDED-GATE TRANSFER DEVICE	FURUKAWA, TOSHIHARU
<u>09127132</u>	<u>6221680</u>	150	07/31/1998	PATTERNED RECESS FORMATION USING ACID DIFFUSION	FURUKAWA, TOSHIHARU
<u>09131828</u>	<u>6319759</u>	150	08/10/1998	METHOD FOR MAKING OXIDE	FURUKAWA, TOSHIHARU
<u>09136586</u>	<u>6207493</u>	150	08/19/1998	FORMATION OF OUT-DIFFUSED BITLINE BY LASER ANNEAL	FURUKAWA, TOSHIHARU
<u>09165503</u>	<u>5949700</u>	150	10/02/1998	FIVE SQUARE VERTICAL DYNAMIC RANDOM ACCESS MEMORY CELL	FURUKAWA, TOSHIHARU
<u>09172865</u>	<u>6063658</u>	150	10/15/1998	METHODS OF MAKING A TRENCH STORAGE DRAM CELL INCLUDING A STEP TRANSFER DEVICE	FURUKAWA, TOSHIHARU
<u>09175304</u>	<u>6090673</u>	150	10/20/1998	DEVICE CONTACT STRUCTURE AND METHOD FOR FABRICATING SAME	FURUKAWA, TOSHIHARU
<u>09182173</u>	<u>6100172</u>	150	10/29/1998	METHOD FOR FORMING A HORIZONTAL SURFACE SPACER AND DEVICES FORMED THEREBY	FURUKAWA, TOSHIHARU
<u>09182388</u>	<u>6096598</u>	150	10/29/1998	METHOD FOR FORMING PILLAR MEMORY CELLS AND DEVICE FORMED THEREBY	FURUKAWA, TOSHIHARU
<u>09183338</u>	<u>6150256</u>	150	10/30/1998	METHOD FOR FORMING SELF-	FURUKAWA,

				ALIGNED FEATURES	TOSHIHARU
<u>09183926</u>	<u>6194268</u>	150	10/30/1998	PRINTING SUBLITHOGRAPHIC IMAGES USING A SHADOW MANDREL AND OFF-AXIS EXPOSURE	FURUKAWA, TOSHIHARU
<u>09192137</u>	<u>6221562</u>	150	11/13/1998	RESIST IMAGE REVERSAL BY MEANS OF SPUN-ON-GLASS	FURUKAWA, TOSHIHARU
<u>09192140</u>	<u>6303272</u>	150	11/13/1998	PROCESS FOR SELF-ALIGNMENT OF SUB-CRITICAL CONTACTS TO WIRING	FURUKAWA, TOSHIHARU
<u>09224759</u>	<u>6759315</u>	150	01/04/1999	METHOD FOR SELECTIVE TRIMMING OF GATE STRUCTURES AND APPARATUS FORMED THEREBY	FURUKAWA, TOSHIHARU
<u>09224760</u>	<u>6207514</u>	150	01/04/1999	METHOD FOR FORMING BORDERLESS GATE STRUCTURES AND APPARATUS FORMED THEREBY	FURUKAWA, TOSHIHARU
<u>09225127</u>	<u>6380027</u>	150	01/04/1999	DUAL TOX TRENCH DRAM STRUCTURES AND PROCESS USING V-GROOVE	FURUKAWA, TOSHIHARU
<u>09243221</u>	<u>6228705</u>	150	02/03/1999	OVERLAY PROCESS FOR FABRICATING A SEMICONDUCTOR DEVICE	FURUKAWA, TOSHIHARU
<u>09247553</u>	<u>6106988</u>	150	02/10/1999	TONER RESIN COMPOSITION AND TONER PREPARED THEREFROM	FURUKAWA, TOSHIHARU
<u>09253455</u>	<u>6426175</u>	150	02/22/1999	FABRICATION OF A HIGH DENSITY LONG CHANNEL DRAM GATE WITH OR WITHOUT A GROOVED GATE	FURUKAWA, TOSHIHARU
<u>09281136</u>	<u>6037194</u>	150	03/29/1999	METHOD FOR MAKING A DRAM CELL WITH GROOVED TRANSFER DEVICE	FURUKAWA, TOSHIHARU
<u>09287410</u>	<u>6376873</u>	150	04/07/1999	VERTICAL DRAM CELL WITH ROBUST GATE-TO-STORAGE NODE ISOLATION	FURUKAWA, TOSHIHARU
<u>09290932</u>	<u>6514840</u>	150	04/13/1999	MICRO HEATING OF SELECTIVE REGIONS	FURUKAWA, TOSHIHARU
<u>09296368</u>	<u>6251755</u>	150	04/22/1999	HIGH RESOLUTION DOPANT/IMPURITY INCORPORATION IN SEMICONDUCTORS VIA A SCANNED ATOMIC FORCE	FURUKAWA, TOSHIHARU

				PROBE	
<u>09296807</u>	<u>6184549</u>	150	04/23/1999	TRENCH STORAGE DYNAMIC RANDOM ACCESS MEMORY CELL WITH VERTICAL TRANSFER DEVICE	FURUKAWA, TOSHIHARU

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**Search Another: Inventor**

Last Name	First Name
<input type="text" value="furukawa"/>	<input type="text" value="toshiharu"/>

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